

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

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Listing of Claims:

Claim 1 (currently amended): A plasma panel ~~(PP)~~ comprising:

- 10 a rear plate;
a front plate parallel ~~to with~~ and spaced apart from the rear plate by a plurality of spacers;
a plurality of electrode pairs ~~disposed in parallel with to~~ each other and disposed over the rear
15 plate; and-
a first dielectric layer having a first predefined pattern covering the plurality of electrode pairs, wherein a recess is formed between two adjacent electrodes of the plurality of
20 electrode pairs; and
a first fluorescent layer covering the first dielectric layer.

Claim 2 (currently amended): The plasma panel of
25 claim 1, wherein the spacing between two adjacent electrodes each of the plurality of electrode pairs has an equal spacing.

Claim 3 (currently amended): The plasma panel of claim
30 1, ~~wherein the electrode pairs are disposed on a bottom surface of the front plate~~ further comprising a second fluorescent layer disposed over a surface of the front

plate, wherein the surface of the front plate faces the rear plate.

5 Claim 4 (currently amended): The plasma panel of claim 3, further comprising a second dielectric layer having a second predefined pattern, sandwiched between the front plate and the second fluorescent layer
~~disposed on a top surface of the rear plate.~~

10 Claim 5 (currently amended): The plasma panel of claim 1, wherein the first dielectric layer covers a partial surface of the rear plate between two adjacent electrodes of the plurality of electrode pairs, and the first fluorescent layer covers the first
15 dielectric layer and the rear plate between the two adjacent electrodes of the plurality of electrode pairs
~~4 further comprising a fluorescent layer covering the second dielectric layer.~~

20 Claim 6 (currently amended): The plasma panel of claim 5, further comprising a third dielectric layer having a third predefined pattern, disposed between the two adjacent electrodes of the plurality of electrode pairs and sandwiched between the first fluorescent
25 layer and the rear plate
~~1 wherein the electrode pairs are disposed on a top surface of the rear plate.~~

Claim 7 (currently amended): The plasma panel of claim 6, wherein the third dielectric layer comprises at
30 least one protrusion
~~further comprising a second dielectric layer having a second predefined pattern disposed on a bottom surface of the front plate.~~

Claim 8 (currently amended): The plasma panel of claim
6, 7 further comprising a third fluorescent layer
disposed over a surface of the front plate, wherein
5 the surface of the front plate faces the rear plate
~~covering the second dielectric layer.~~

Claim 9 (canceled)

10 Claim 10 (currently amended): The plasma panel of claim
8, 1 further comprising a ~~fluorescent layer covering~~
~~the first dielectric layer~~ fourth dielectric layer
having a fourth predefined pattern, sandwiched between
the front plate and the third fluorescent layer.

15 Claim 11 (currently amended): The plasma panel of
claim ~~10~~ 1 wherein the first fluorescent layer is a
phosphorous layer.

20 Claim 12 (canceled)

Claim 13 (new): The plasma panel of claim 3, wherein
the second fluorescent layer is a phosphorous layer.

25 Claim 14 (new): The plasma panel of claim 8, wherein
the third fluorescent layer is a phosphorous layer.

Claim 15 (new): The plasma panel of claim 1, wherein
a discharge gap is formed between two adjacent
30 electrodes of the plurality of electrode pairs.

Claim 16 (new): A plasma panel comprising:

a rear plate;
a front plate parallel to and spaced apart from
the rear plate by a plurality of spacers;
a plurality of electrode pairs parallel to each
5 other and disposed over a surface of the
front plate, wherein the surface of the
front plate faces the rear plate;
a dielectric layer having a predefined pattern
covering the plurality of electrode pairs,
10 wherein a recess is formed between two adjacent
electrodes of the plurality of electrode pairs;
and
a fluorescent layer covering the dielectric
layer.

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